



Applicant:
Application No.:
For:
Agent:

Martin Langhammer
09/826,527
DSP PROCESSOR ARCHITECTURE WITH WRITE
DATAPATH WORD CONDITIONING AND ANALYSIS
Hassan Albakri (Limited Recognition)

Docket No.: 174/205
Filed: April 4, 2001 Sheet 1 of 8
Express Mail No. EV619635112US

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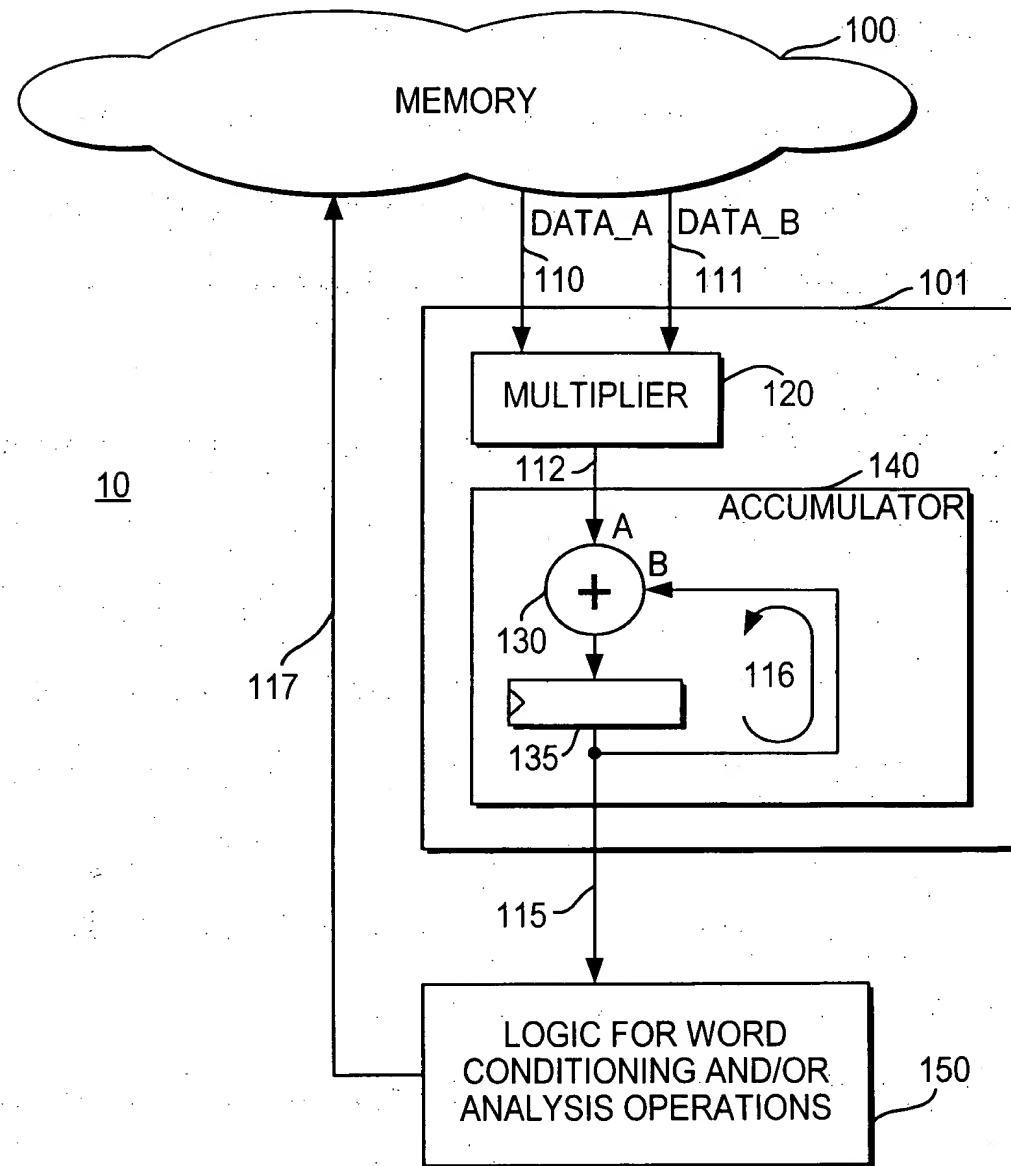


FIG. 1a

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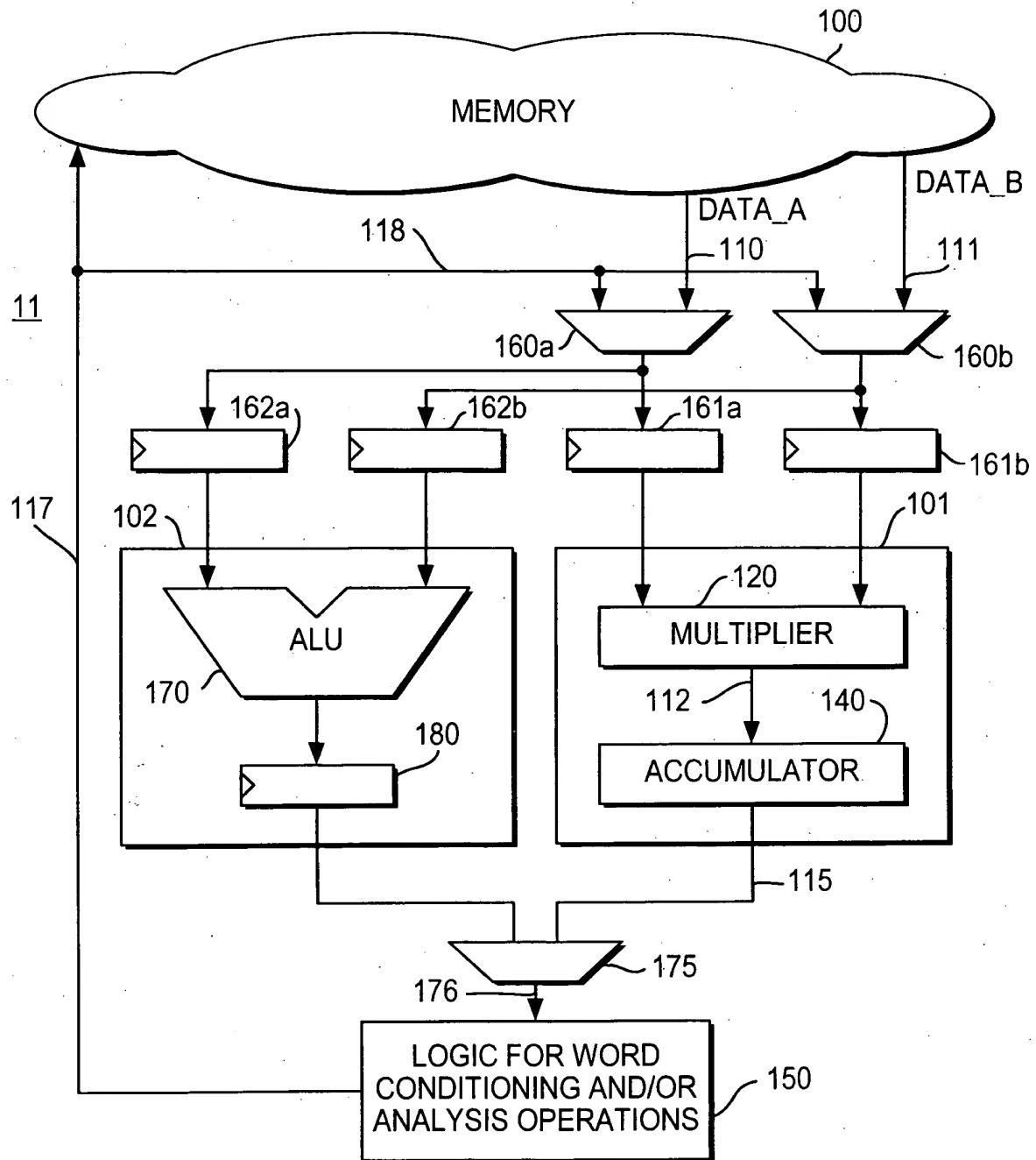


FIG. 1b



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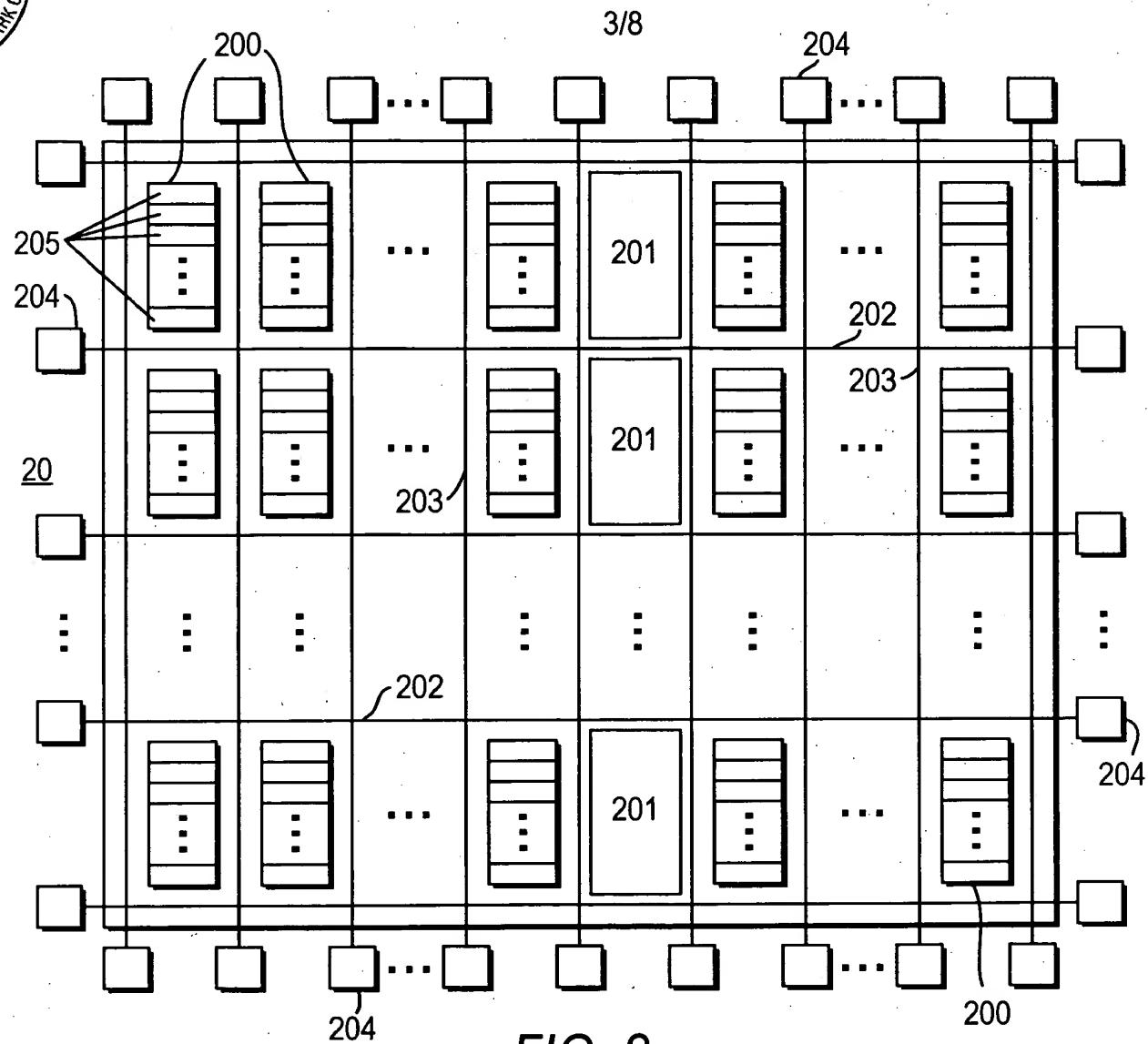


FIG. 2

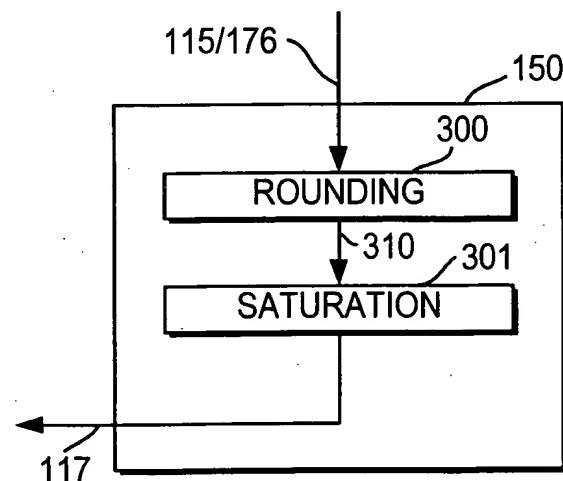


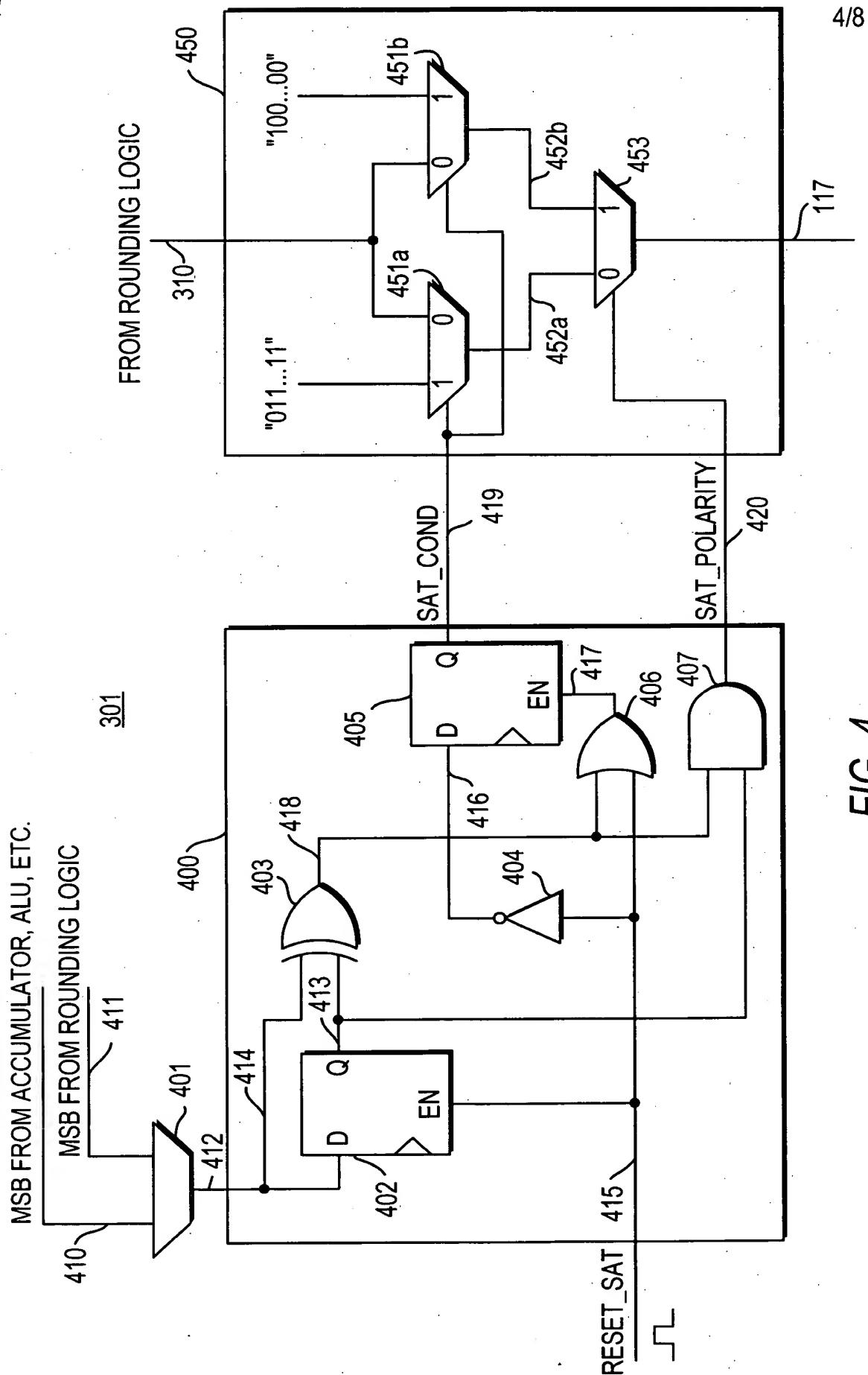
FIG. 3

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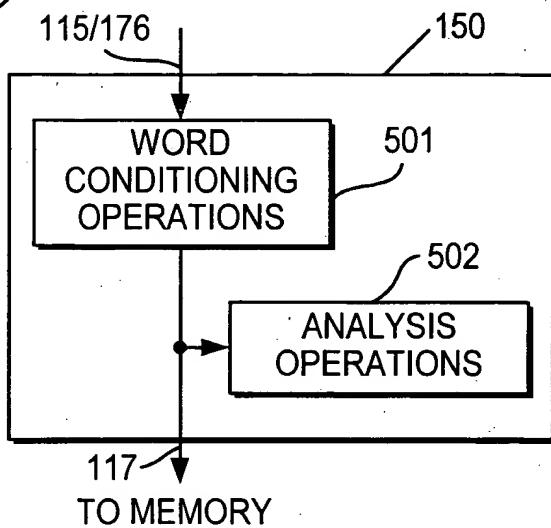


FIG. 5a

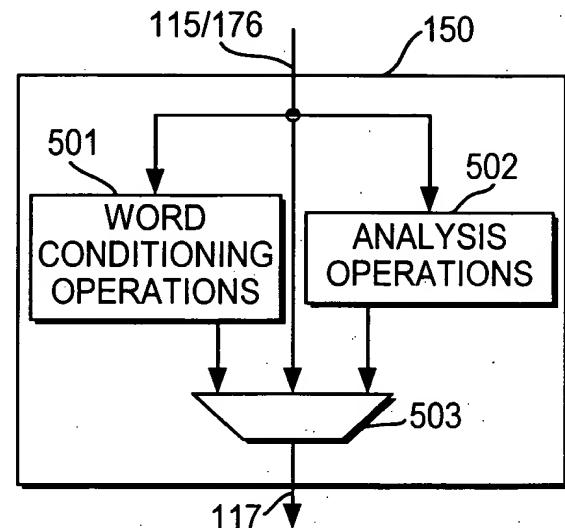


FIG. 5b

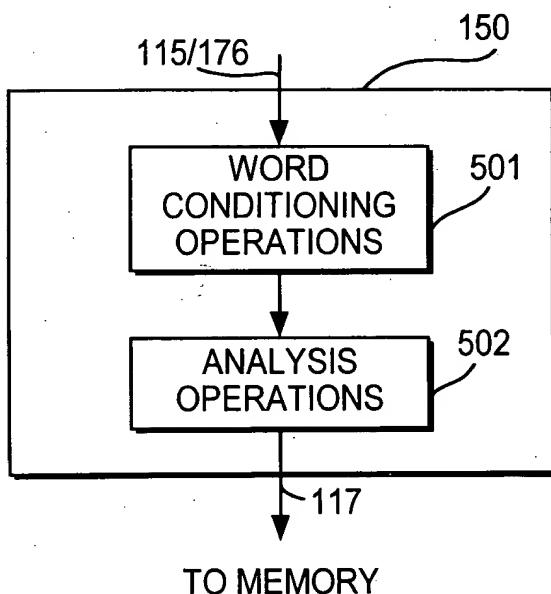


FIG. 5c

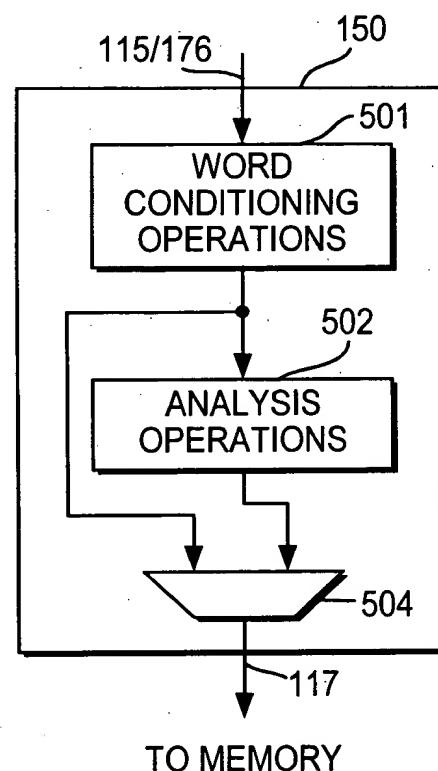


FIG. 5d



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DSP PROCESSOR A
DATAPATH WORD
Hassan Albakri (Limp)

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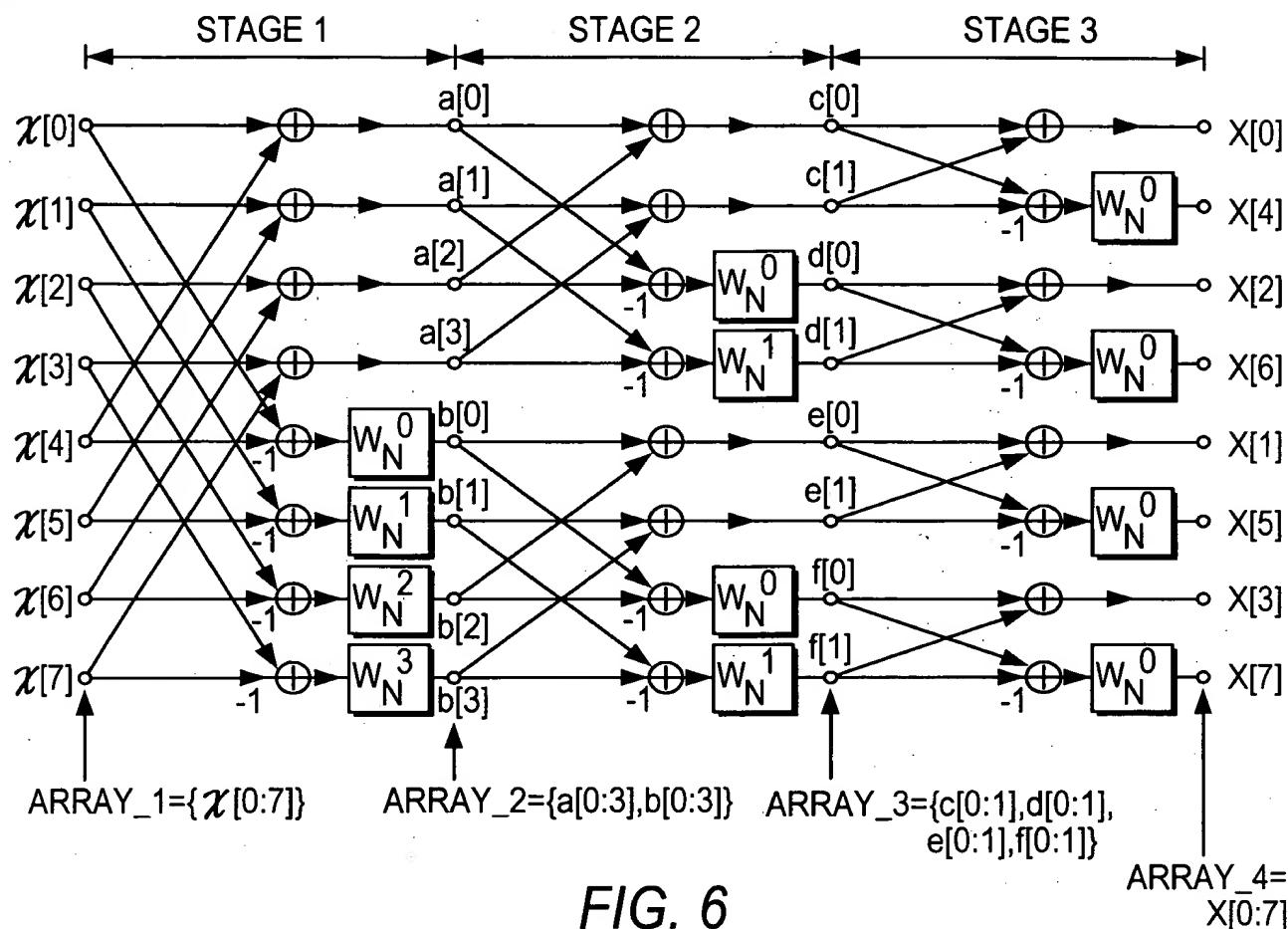


FIG. 6

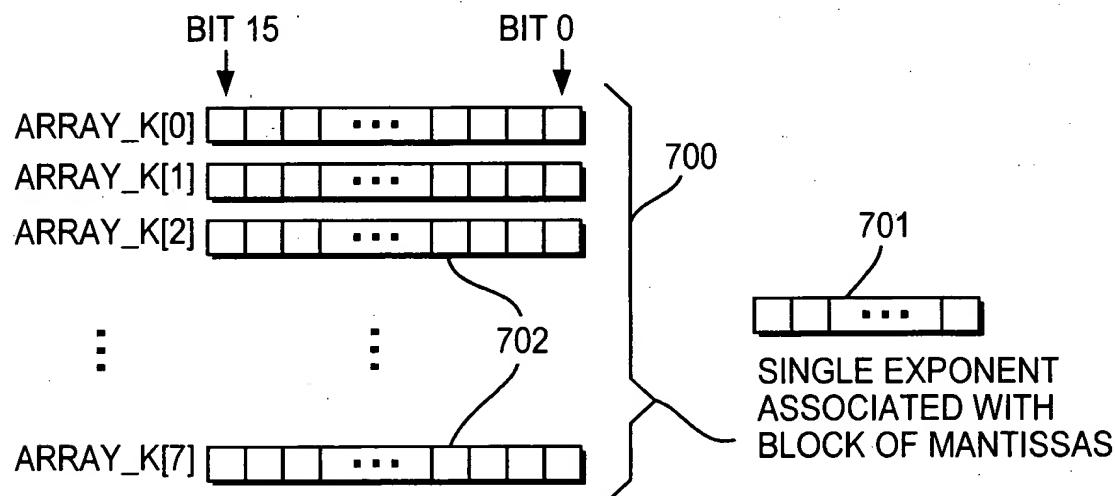


FIG. 7



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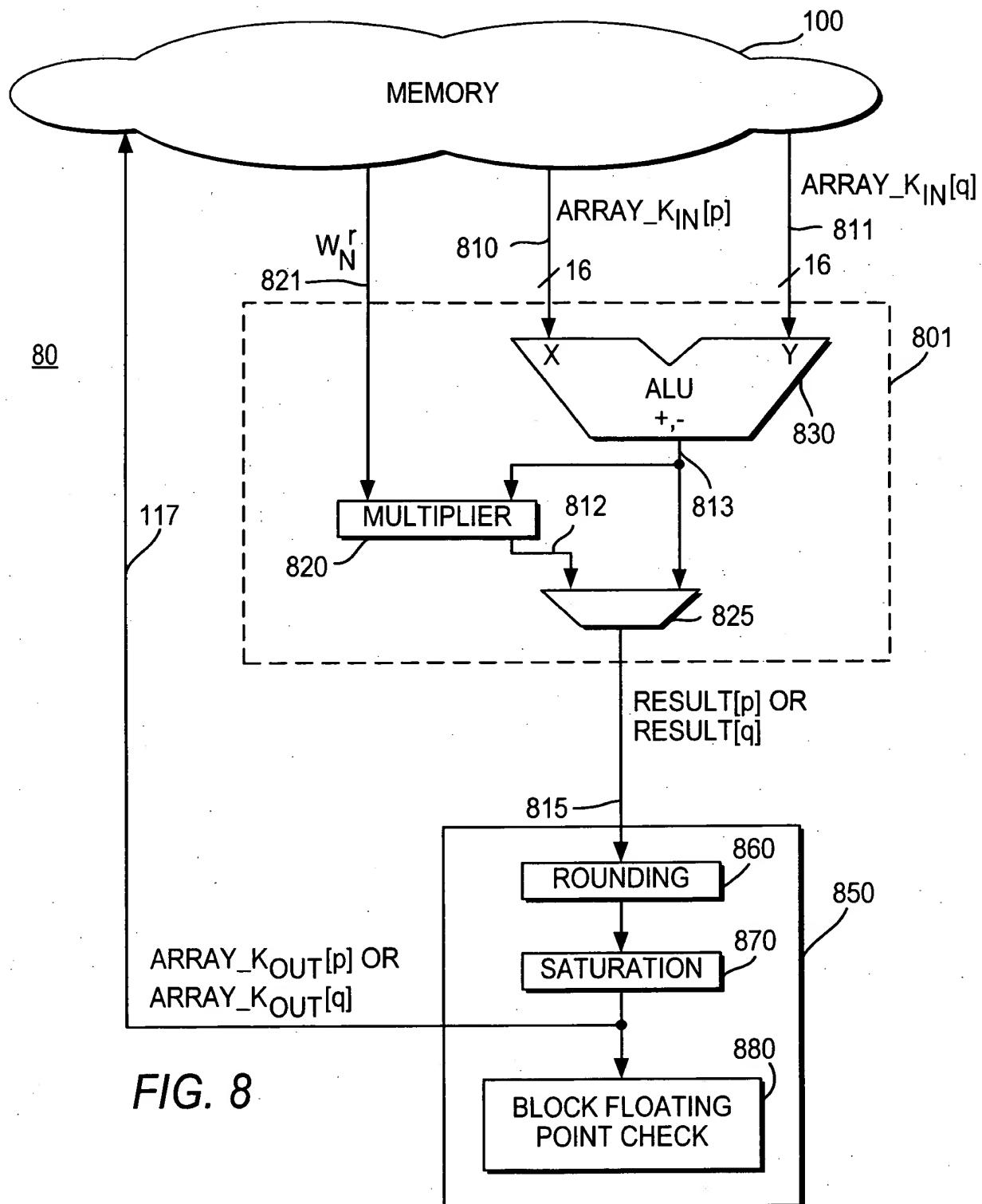


FIG. 8



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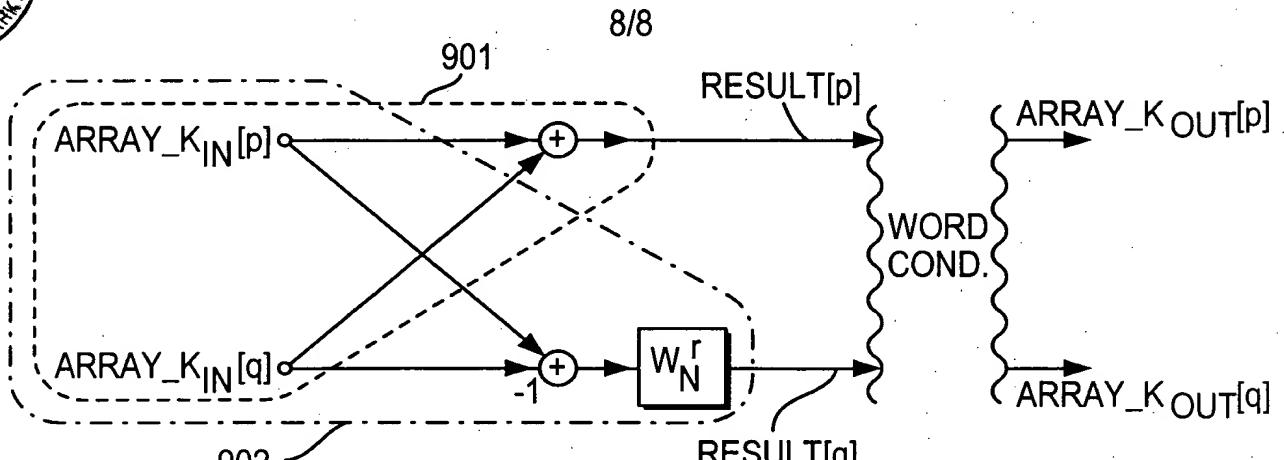


FIG. 9

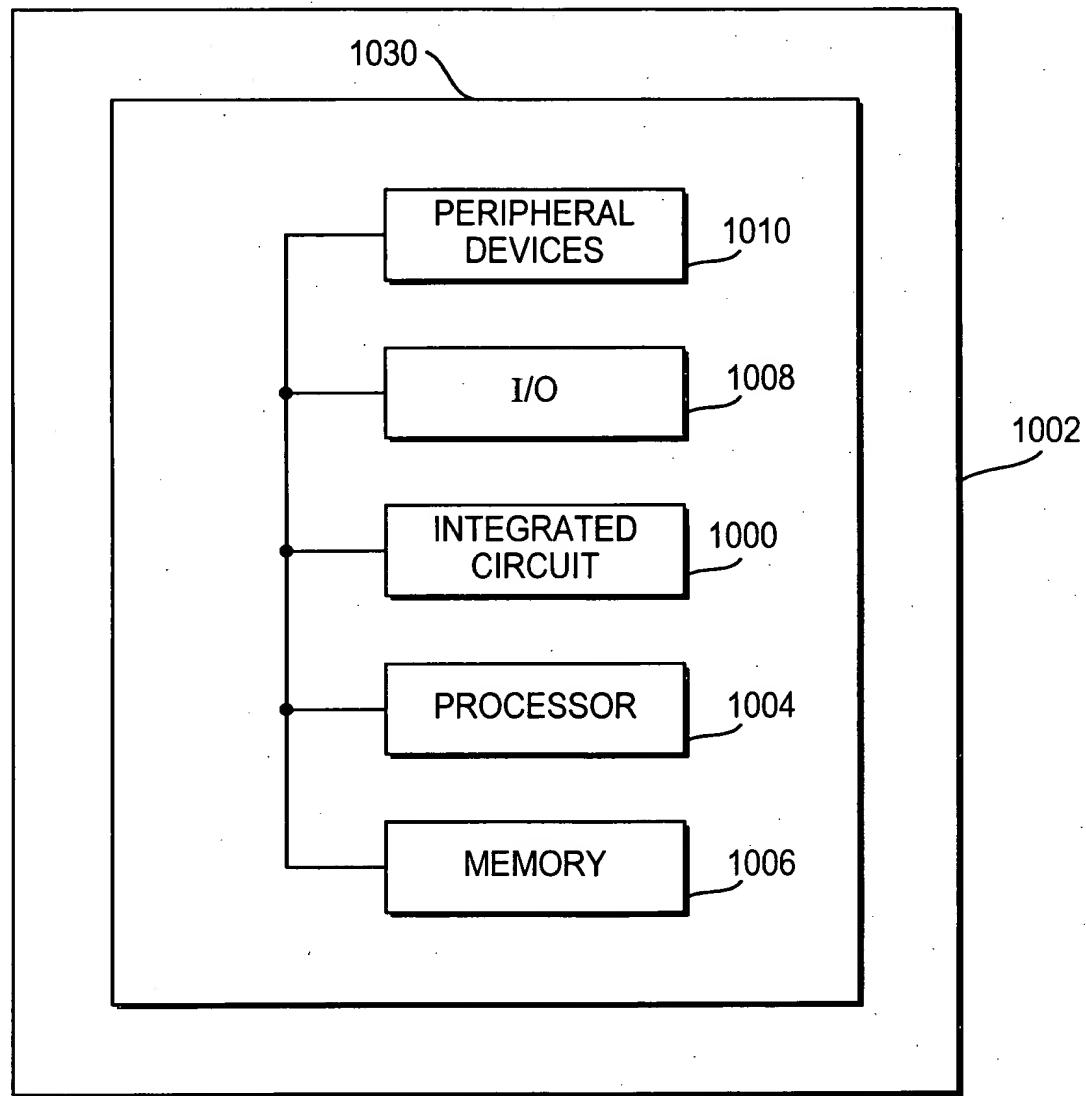


FIG. 10